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## Reachability for Finite-State Process Algebras Using Static Analysis

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## Main idea



- Perform Static Analysis (in particular, Data Flow Analysis) on the syntax of a process algebra;
- Use the results to compute an overapproximation of the reachable states;
- If the state in question possibly reachable, construct states reachable from the initial state in one step;
- Reassess our overapproximation of reachability;
- Continue until no more states or overapproximation does not contain the state in question.

- 1 Process algebra with CSP synchronisation model (PA)
- 2 Data Flow Analysis of PA
- 3 Correct and complete reachability algorithm

# Syntax of PA



Syntactic classes: **prefixed process variables**, **prefixed expressions**, **sums**, **recursive process definitions**, **terminal process**, **parallel compositions**, **scope restrictions**.  $P$  is a linear PA process.  $E$  is an PA process. An PA program is a uniquely labelled PA process with unique process variables.

$$\begin{array}{l}
 P ::= a^\ell.X \quad | \\
 \quad a^\ell.P \quad | \\
 \quad P + P \quad | \\
 \quad \frac{X := P}{\mathbf{0}} \quad | \\
 E ::= P \quad | \\
 \quad E \parallel A \parallel E \quad | \\
 \quad \text{hide } A \text{ in } P
 \end{array}$$

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- *Parallel processes:*  $P_1 \parallel A \parallel P_2 \xrightarrow[c]{a} P'_1 \parallel A \parallel P_2$  if  $P_1 \xrightarrow[c]{a} P'_1$  and  $a \notin A$ ;  
 $P_1 \parallel A \parallel P_2 \xrightarrow[c_1 \cup c_2]{a} P'_1 \parallel A \parallel P'_2$  if  $P_1 \xrightarrow[c_1]{a} P'_1$ ,  $P_2 \xrightarrow[c_2]{a} P'_2$  and  $a \in A$ ;

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- *Internalisation*:  $\text{hide } A \text{ in } P \xrightarrow[c]{\tau} \text{hide } A \text{ in } P'$  if  $P \xrightarrow[c]{a} P'$  and  $a \in A$ ;  $\text{hide } A \text{ in } P \xrightarrow[c]{a} \text{hide } A \text{ in } P'$  if  $P \xrightarrow[c]{a} P'$  and  $a \notin A$ ;

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- *Process definition:*  $\underline{X := P} \xrightarrow[c]{a} P'$  if  $P[X/\underline{X := P}] \xrightarrow[c]{a} P'$ .

## Examples of PA systems



- $\underline{X := a^{\ell_1}.X + b^{\ell_2}.0} \xrightarrow[\{\ell_1\}]{a} \underline{X := a^{\ell_1}.X + b^{\ell_2}.0}$
- $\underline{X := a^{\ell_1}.X + b^{\ell_2}.0} \xrightarrow[\{\ell_2\}]{b} 0$

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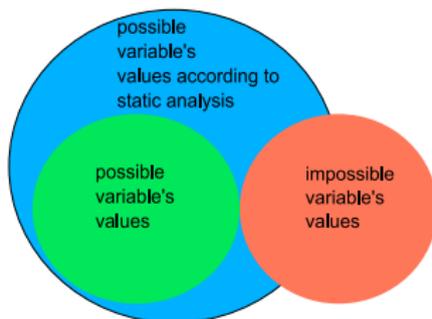
- $\underline{X := a^{\ell_1}.X} \parallel a \parallel \underline{Y := a^{\ell_2}.b^{\ell_3}.Y} \xrightarrow[\{\ell_1, \ell_2\}]{a}$   
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# Static Analysis



- Developed in the area of Program Analysis: Control Flow Analysis, Data Flow Analysis etc.
- Purpose: verifying a program by analysing program's code;



- Transferred to process calculi: verify the semantics without building full LTS, by analysing the syntax;

# Data Flow Analysis of PA



- Based on **Data Flow Analysis for CCS** by H.R.Nielson and F.Nielson from 2006
- Further process calculi: BioAmbients, broadcast calculus bKlaim
- Reason: handling state space explosion
- Adjustment of the traditional Data Flow Analysis to process calculi

$$f_{state}(E) = (E \setminus kill_{state}) \cup gen_{state}$$

- Labeled Transition System states instead of program points

# Transitions from $E$ and Data Flow Analysis of $E$



$$E \xrightarrow[C]{\alpha} E'$$

- Transition entry: *exposed labels of  $E$*
- Transition exit: *exposed labels \ killed labels  $\cup$  generated labels*
- Chain  $C$  corresponds to action name  $\alpha$
- All the labels in the chain  $C$  are exposed

# Operators on PA expressions



- *Exposed* operator  $\mathcal{E}$  returns labels which may “fire” in the next transition
- *Kill* operator  $\mathcal{K}$  returns for a particular label those labels which must cease to be available for execution after the corresponding label has been executed
- *Generate* operator  $\mathcal{G}$  returns for a particular label those labels which may become available for execution after the corresponding label has been executed
- *Chains* operator  $\mathcal{T}$  returns labels to be executed together due to synchronisation

# Data Flow Analysis example



$$E \triangleq \underline{X := a^{l_1}.b^{l_2}.X + c^{l_3}.r^{l_4}.X} \parallel \{a\} \parallel \underline{Y := a^{l_5}.Z := d^{l_6}.Z}$$

$$a \downarrow \{l_1 \mapsto 1, l_5 \mapsto 1\}$$

$$E' \triangleq \underline{b^{l_2}.X := a^{l_1}.b^{l_2}.X + c^{l_3}.r^{l_4}.X} \parallel \{a\} \parallel \underline{Z := d^{l_6}.Z}$$

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- exposed of  $E'$ :  $\{l_2 \mapsto 1, l_6 \mapsto 1\}$

$$\text{exposed}(E) \setminus \text{kill}(l_1) \setminus \text{kill}(l_5) \cup \text{generate}(l_1) \cup \text{generate}(l_5) = \text{exposed}(E')$$

# Main Results for Data Flow Analysis of PA programs



- Generate, kill and chains operators on  $F$  predict all (and only) transitions from  $F$
- Chains, generate, kill operators, chains-to-names correspondence etc. are stable under SOS transitions;
- the results of the operators on an PA program are enough to reproduce its semantics., i.e. Data Flow Analysis of PA programs not only correct, but also precise.

### *Theorem*

*Given an PA program  $F$ , then for all  $E$  such that  $F \xrightarrow{*} E$ ,  $\Gamma$  and  $\Lambda$  mappings for labels and process definitions computed on  $F$ , we have:*

- *each label is exposed in  $E$  at most once;*
- *$E \xrightarrow[C]{a} E'$  if and only if  $C \in \mathfrak{T}_\Lambda[F]$  and  $C \subseteq \mathcal{E}_\Gamma[E]$ ;*
- *$\mathcal{E}_\Gamma[E'] = \mathcal{E}_\Gamma[E] \setminus (\cup_{\ell \in C} \mathcal{K}[F](\ell)) \cup (\cup_{\ell \in C} \mathcal{G}_\Gamma[F](\ell))$ .*

# Idea: compute overapproximation of reachable labels



- All labels exposed in the initial states are reachable;
- For other labels to be reachable there should exist a chain such that all labels in it are reachable;
- Algorithm: recursively delete from the set of reachable labels those that do not have any chain with all constituting labels in it being in the set of reachable labels

## Algorithm: initialisation sep



```
proc init( $F$ ) is
  for all  $\ell \in \text{Labs}(F)$  do
     $gchains(\ell) := \{C \in \mathfrak{T}_\Lambda[F] \mid \exists \ell' \in C \text{ such that } \ell \in \mathcal{G}_r[F](\ell')\}$ 
  return  $gchains$ 
```

## Algorithm: refinement step



```
proc refine( $F, S, gchains$ ) is
 $L := S; gchains' := gchains;$ 
while  $\exists \ell \in Labs(F)$  such that  $(gchains'(\ell) = \emptyset) \wedge (\ell \notin S)$  do
  for all  $\ell' \in Labs(F)$  do
     $gchains'(\ell') := gchains'(\ell') \setminus \{C \in \mathfrak{T}_\Lambda[F] \mid \ell \in C\}$ 
  for all  $\ell \in Labs(F)$  do
    if  $gchains'(\ell) \neq \emptyset$  then
       $L := L \cup \{\ell\};$ 
return  $L, gchains'$ 
```

## Example of computing reachable labels



For  $F \triangleq (b^{l_1}.a^{l_2}.c^{l_3}.0 + a^{l_4}.a^{l_5}.d^{l_6}.0) \parallel \{a, b\} \parallel a^{l_7}.0$

we have

$$\text{init}(F) = \{l_1 \mapsto \emptyset, l_2 \mapsto \emptyset, l_3 \mapsto \{\{l_2, l_7\}\}, l_4 \mapsto \emptyset, l_5 \mapsto \{\{l_4, l_7\}\}, l_6 \mapsto \{\{l_5, l_7\}\}, l_7 \mapsto \emptyset\}$$

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With  $(L, gchains) = \text{refine}(F, \mathcal{E}_F \llbracket F \rrbracket, \text{init}(F))$ ,

we have

$$gchains = \{l_1 \mapsto \emptyset, l_2 \mapsto \emptyset, l_3 \mapsto \emptyset, l_4 \mapsto \emptyset, l_5 \mapsto \{\{l_4, l_7\}\}, l_6 \mapsto \{\{l_5, l_7\}\}, l_7 \mapsto \emptyset\}$$

and therefore

$$L = \{l_1, l_4, l_5, l_6, l_7\}.$$

# Algorithm for reachability of $S_?$ from the initial state $S_{in}$ of $F$



- Do some sanity check first: whether  $S_?$  is impossible because labels in it exclude each other or whether  $S_{in} = S_?$
- Add  $S_{in}$  to the Worklist
- Choose some  $S$  from the Worklist and compute overapproximation  $L$  of labels reachable from  $S$
- If  $S_? \not\subseteq L$  then break;
- Otherwise create all the transitions  $S \rightarrow S''$
- If one of  $S''$  is equal to  $S_?$  or we have encountered all  $S''$ s before then we are done
- Otherwise add all not encountered before  $S''$  to the Worklist
- Go to p. 2

## Examples



$$b^{l_1}.a^{l_2}.c^{l_3}.0 + a^{l_4}.a^{l_5}.d^{l_6}.0 \parallel \{a, b\} \parallel a^{l_7}.0 \xrightarrow[\{l_4, l_7\}]{a} a^{l_5}.d^{l_6}.0 \parallel \{a, b\} \parallel 0$$

In  $(a^{l_1} \dots b^{l_n}.0 + c^{l'_1} \dots d^{l'_n}.0) \parallel \emptyset \parallel e^{l''_1} \dots f^{l''_n}.0$

the branch  $c^{l'_1} \dots d^{l'_n}.0$  interleaved with  $e^{l''_1} \dots f^{l''_n}.0$

is not explored while determining the reachability of e.g.  $l_n$

## Proved results



### *Lemma*

*if  $F \xrightarrow{*} E \xrightarrow{*} E'$  for some  $E$  and  $E'$  and  $L$  is computed by refine on  $E$  then  $\mathcal{E}_{\Gamma} \llbracket E' \rrbracket \subseteq L$*

### *Theorem*

*Given a PA program  $F$ , then  $F \xrightarrow{*} E$  iff  $\text{reach}(F, \mathcal{E}_{\Gamma} \llbracket E \rrbracket) = \text{true}$ .*

## Conclusions



- We have presented a complete reachability algorithm for process algebras based on Static Analysis methods
- Algorithm determines dead branches that cannot lead to the state in question
- Can be used with partial knowledge of the initial and goal states (i.e. with subsets of exposed labels)
- With efficient data structures additional overhead quadratic in the length of the syntax
- Algorithm can be used just for initial state / some of the states, i.e. in the usual way Static Analysis results are used

## Future work



- Other systems allowing for compositional verification – other process calculi etc.;
- Infinite semantic models (i.e., utilising Control Flow Analysis);
- Other properties checked – e.g. repeated reachability;
- Property-directed computation;
- Further reduction of the state space, e.g. through computing independent actions;
- Implementation and case studies.

Thank you for attention!  
Questions?